

(19)

(11) Publication number:

05344

Generated Document

PATENT ABSTRACTS OF JAPAN

(21) Application number: 04149074

(51) Intl. Cl.: H04B 10/16 H04B 3/46 H04

(22) Application date: 09.06.92

(30) Priority:

(43) Date of application 24.12.93 publication:

(71) Applicant: NEC CORP

(72) Inventor: YAMASHITA SHUJI

(74) Representative:

CONTROL CIRCUIT (54) LOOPBACK

contracting states: (84) Designated

https://www.delphion.com/cgi-bin/viewpat.cmd/JP05344074A2

07.08.01

FOR OPTICAL REPEATER

(57) Abstract:

PURPOSE: To prevent a loop island caused at loopback.

CONSTITUTION: A control circuit consists of level detection circuits 32, 34 receiving a control signal 105 from an incoming transmission line and receiving a control signal 108 from an outgoing transmission line respectively and detecting the level, a switch 33 selecting a control signal whose level is detected, a reception circuit 38 decoding an on/off signal of loopback from the selected control signal, a

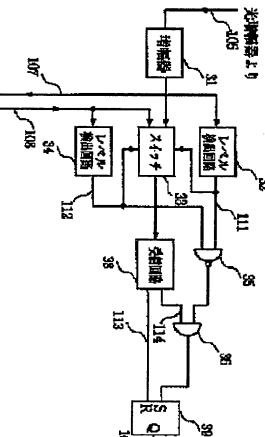
https://www.delphion.com/cgi-bin/viewpat.cmd/JP05344074A2

NAND gate 35 and an AND gate 36 blocking an on-signal of loopback when an output signal 112 of the level detection circuit 34 is in existence an a signal latch S/R flip-flop 37. When other loopback circuit is formed with the control signal 108 from the outgoing transmission line, no loopback control signal 106 is outputted.

COPYRIGHT: (C) 1993, JPO& Japio

• :

7 -



07.08.01

, 😤 U3344U14 A

Side Say S

07.08.01

https://www.delphion.com/cgi-bin/viewpat.cmd/JP05344074A2

THIS PAGE BLANK (USPTO)